



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Budong You et al. Art Unit : 2812
Serial No. : 10/714,271 Examiner : Ron Everett Pompey
Filed : November 13, 2003
Title : A METHOD OF FABRICATING A LATERAL DOUBLE-DIFFUSED MOSFET
(LDMOS) TRANSISTOR AND A CONVENTIONAL CMOS TRANSISTOR

Mail Stop RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INTERVIEW SUMMARY

The undersigned representative thanks the Examiner for the courtesy of the telephonic interview held on June 8, 2006.

The substance of the interview included a discussion of claims 1-26 and the election of Group I (Claims 1-17).

Respectfully submitted,

Date: 6/26/06

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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